

H5/2813
Electon
Aged
7/26/99

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Naoya SASHIDA et al.**

Serial No.: **09/321,605**

Group Art Unit: **2813**

Filed: **May 28, 1999**

Examiner: **C. Bowers, Jr.**

For: **SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME**



RESPONSE TO THE RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

July 22, 1999

Sir:

In response to the Restriction Requirement dated July 1, 1999, Applicants hereby elect to prosecute Group I, Claims 1-16, (drawn to a method of making a semiconductor device, classified in class 438, subclass 240) without traverse. Applicants reserve the right to file a divisional application directed to non-elected claims.

If any additional fees are due with regard to this paper, please charge counsel's Deposit Account No. 01-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI,
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